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09/862689  
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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
FA SCANNED 11/7/11 Q.A. Am	

APPLICATION NO. 09/862689	CONT/PRIOR	CLASS <del>361</del> 379	SUBCLASS 349.01	ART UNIT 284-1 2643	EXAMINER JAMAL Satch
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## APPLICANTS

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Raphael Rahamim

**TITLE**

Circuit board capacitor structure for forming a high voltage isolation barrier

<b>ISSUING CLASSIFICATION</b>									
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>					
<b>CLASS</b>		<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>				
<b>INTERNATIONAL CLASSIFICATION</b>									

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
				<b>ISSUE FEE</b> Amount Due Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)				
				<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	
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